

REMARKS

Claims 1-30 are pending in the present application.

Applicant notes with appreciation that the Office Action indicates that the arguments presented in the Response filed on October 29, 2004 are persuasive. The only outstanding claim rejection is a rejection of claims 1 – 30 under 35 U.S.C. § 112, first and second paragraphs, as presented below.

I. OBJECTION TO THE DRAWINGS

The drawings are objected under 37 C.F.R 1.83(a) because the drawings allegedly do not show the “drive circuit.” Applicant submits that the invention is directed to a “drive circuit,” and that the “drive circuit” is clearly shown in the figures. For example, Figs. 1, 2 and 5 show configurations of the drive circuit (see bottom of page 11 through the top of page 12 of the specification). Therefore, Applicant submits that this rejection is improper and respectfully requests that it be withdrawn.

II. REJECTIONS OF THE CLAIMS UNDER 35 U.S.C. § 112

A. 35 U.S.C. § 112, first paragraph

Claims 1-30 are rejected under 35 U.S.C. § 112, first paragraph. The Examiner asserts that the limitation in independent claims 1, 12 and 21, “said drive circuit comprising adjusting means for adjusting potential differences between the pixel electrodes and a common electrode, wherein the adjusting means is composed of voltage level altering means for shifting voltage levels of

the source signals supplied by the source driver equally for all of the pixel electrodes,” is not clearly described anywhere in the specification.

Applicant submits that these claim features are clearly described in page 19, line 7 through page 27 of the subject specification. This section of the specification clearly describes the arrangement of resistors and other circuit components that comprise the claimed adjusting means and altering means.

The Examiner also asserts that the specification fails to define the potential differences between each and every pixel electrode and common electrode. Applicant submits that the potential differences between the pixel electrodes 7 and common electrode 8 are defined on page 15, line 17 through page 16, line 1 of the subject specification. Also, these potential differences are described on page 18, line 24 through page 19, line 6; and page 22, lines 1-17 of the subject specification.

Further, the Examiner asks: “how the adjusting means shifts the voltage of the source signals supplied by the source driver equally for all of the pixel electrodes.” This feature is described on page 34, lines 14-23 of the subject specification.

Therefore, Applicant submits that the claims are sufficiently described in the specification. Accordingly, Applicants submits that the rejection of claims 1-30 under 35 U.S.C. § 112, first paragraph, is improper and respectfully requests that this rejection be withdrawn.

B. 35 U.S.C. § 112, second paragraph

Claims 1- 30 also are rejected under 35 U.S.C. § 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which application regards as the invention. As rationale for this rejection, the Examiner restates the reasons asserted in the rejection under 35 U.S.C. § 112, first paragraph. Therefore, Applicant submits that this rejection is improper as presented above in response to the rejection of claims 1- 30 under 35 U.S.C. § 112, first paragraph. Accordingly, Applicants submits that the rejection of claims 1-30 under 35 U.S.C. § 112, second paragraph, is improper and respectfully requests that this rejection be withdrawn.

Also, the Examiner asks: “What is the difference between the adjusting means and the altering means?” and “how come the adjusting means is composed of voltage level altering means when the specification discloses that the reference voltage generator circuit (adjusting means, voltage level altering means, reference voltage generator means)?”. Applicant submits that the adjusting means is a broad claim element that includes the altering means (see page 26, line 22 – page 27, line 2 of the subject specification; and page 31, lines 2-8). Because the reference voltage generator circuit includes the adjusting means, the reference voltage generator circuit also includes the altering means, as explained on page 15, lines 2-7 of the subject specification. In order to clarify the relationship between the reference voltage generator circuit, the adjusting means and the altering means, Applicant has amended the first full paragraph on page 31 of the subject specification, as shown above.

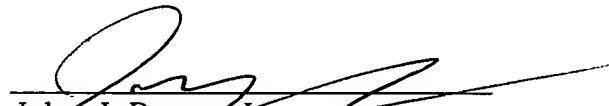
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Therefore, Applicant submits that the claims are sufficiently clear, and that the rejection of claims 1-30 under 35 U.S.C. § 112, second paragraph, is improper and respectfully requests that it be withdrawn.

Based on the foregoing, Applicant submits that the present application is in condition for allowance and allowance is respectfully solicited. If the Examiner believes that the prosecution of this case could be expedited through a telephone interview, he is kindly invited to contact the undersigned at the phone number listed below.

Applicant believes that no additional fees are due for the subject application. However, if for any reason a fee is required, a fee paid is inadequate or credit is owed for any excess fee paid, you are hereby authorized and requested to charge Deposit Account No. 04-1105.

Respectfully submitted,


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Date: August 16, 2005
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